

Day : Thursday  
Date: 8/19/2004

# PALM INTRANET

Time: 14:21:39

## Inventor Name Search Result

Your Search was:

Last Name = CUMMINGS

First Name = URI

Application#	Patent#	Status	Date Filed	Title	Inventor Name 17
<a href="#">60487536</a>	Not Issued	020	07/14/2003	ASYNCHRONOUS STATIC RANDOM ACCESS MEMORY	CUMMINGS, URI
<a href="#">60352131</a>	Not Issued	159	01/25/2002	ASYNCHRONOUS CROSSBAR CIRCUIT WITH DETERMINISTIC OR ARBITRATED CONTROL	CUMMINGS, URI
<a href="#">60058662</a>	Not Issued	159	09/12/1997	PIPELINED COMPLETION FOR ASYNCHRONOUS COMMUNICATION	CUMMINGS , URI V.
<a href="#">60052714</a>	Not Issued	159	07/16/1997	THE DESIGN OF A ASYNCHRONOUS MIPS R3000 MICROPROCESSOR	CUMMINGS , URI R.
<a href="#">60036243</a>	Not Issued	159	01/24/1997	TRAVELING WAVE REFLECTIVE MODULATORS	CUMMINGS , URI
<a href="#">10667152</a>	Not Issued	030	09/16/2003	ASYNCHRONOUS MULTIPLE-ORDER ISSUE SYSTEM ARCHITECTURE	CUMMINGS, URI
<a href="#">10634597</a>	Not Issued	030	08/04/2003	ASYNCHRONOUS SYSTEM-ON-A-CHIP INTERCONNECT	CUMMINGS, URI
<a href="#">10335343</a>	Not Issued	161	12/31/2002	PIPELINED COMPLETION FOR ASYNCHRONOUS COMMUNICATION	CUMMINGS, URI V.
<a href="#">10294044</a>	Not Issued	020	07/18/2001	RESHUFFLED COMMUNICATIONS PROCESSES IN PIPELINED ASYNCHRONOUS CIRCUITS	CUMMINGS, URI
<a href="#">10237406</a>	Not Issued	020	09/06/2002	ASYNCHRONOUS CROSSBAR WITH DETERMINISTIC OR ARBITRATED CONTROL	CUMMINGS, URI
<a href="#">10237358</a>	Not Issued	020	09/06/2002	ASYNCHRONOUS CROSSBAR WITH DETERMINISTIC OR ARBITRATED CONTROL	CUMMINGS, URI

<a href="#">10137035</a>	<a href="#">6658550</a>	150	04/30/2002	PIPELINED ASYNCHRONOUS PROCESSING	CUMMINGS, URI
<a href="#">10136025</a>	Not Issued	020	04/30/2002	ASYNCHRONOUS CROSSBAR WITH DETERMINISTIC OR ARBITRATED CONTROL	CUMMINGS, URI
<a href="#">09496128</a>	<a href="#">6502180</a>	150	02/01/2000	ASYNCHRONOUS CIRCUITS WITH PIPELINED COMPLETION PROCESS	CUMMINGS, URI V.
<a href="#">09151334</a>	<a href="#">6038656</a>	150	09/11/1998	PIPELINED COMPLETION FOR ASYNCHRONOUS COMMUNICATION	CUMMINGS , URI V.
<a href="#">09118140</a>	<a href="#">6381692</a>	150	07/16/1998	PIPELINED ASYNCHRONOUS PROCESSING	CUMMINGS , URI
<a href="#">08999194</a>	<a href="#">5886807</a>	150	12/19/1997	TRAVELING-WAVE REFLECTIVE ELECTRO OPTIC MODULATOR	CUMMINGS , URI

**Inventor Search Completed:** No Records to Display.

<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>	<input type="button" value="Search"/>
	<input type="text" value="CUMMINGS"/>	<input type="text" value="URI"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Thursday  
Date: 8/19/2004



# PALM INTRANET

Time: 14:25:14

## Inventor Name Search Result

Your Search was:

Last Name = LINES

First Name = ANDREW

Application#	Patent#	Status	Date Filed	Title	Inventor Name 30
<a href="#">60487536</a>	Not Issued	020	07/14/2003	ASYNCHRONOUS STATIC RANDOM ACCESS MEMORY	LINES, ANDREW
<a href="#">60444820</a>	Not Issued	159	02/03/2003	ASYNCHRONOUS INTERCONNECT SYSTEM	LINES, ANDREW
<a href="#">60429153</a>	Not Issued	159	11/25/2002	MESSAGE UNIT	LINES, ANDREW
<a href="#">60411717</a>	Not Issued	159	09/16/2002	ASYNCHRONOUS MULTIPLE-ORDER ISSUE PROCESSOR ARCHITECTURE	LINES, ANDREW
<a href="#">60408175</a>	Not Issued	159	08/30/2002	PSEUDO-RANDOM DRAM PACKET STORE	LINES, ANDREW
<a href="#">60404359</a>	Not Issued	159	08/15/2002	P2N SIZING TOOL - PATH BASED SIZING	LINES, ANDREW
<a href="#">60403045</a>	Not Issued	159	08/13/2002	IN-LINE ERROR CORRECTION TECHNIQUES IN DATA PROCESSING SYSTEMS	LINES, ANDREW
<a href="#">60357201</a>	Not Issued	159	02/12/2002	ASYNCHRONOUS-SYNCHRONOUS CONVERSION CIRCUITS	LINES, ANDREW
<a href="#">60352131</a>	Not Issued	159	01/25/2002	ASYNCHRONOUS CROSSBAR CIRCUIT WITH DETERMINISTIC OR ARBITRATED CONTROL	LINES, ANDREW
<a href="#">60093840</a>	Not Issued	159	07/22/1998	PIPELINED ASYNCHRONOUS CIRCUITS	LINES , ANDREW M.
<a href="#">60058662</a>	Not Issued	159	09/12/1997	PIPELINED COMPLETION FOR ASYNCHRONOUS COMMUNICATION	LINES , ANDREW M.
<a href="#">60052714</a>	Not Issued	159	07/16/1997	THE DESIGN OF A ASYNCHRONOUS MIPS R3000 MICROPROCESSOR	LINES , ANDREW
<a href="#">10667152</a>	Not	030	09/16/2003	ASYNCHRONOUS MULTIPLE-	LINES, ANDREW

	Issued			ORDER ISSUE SYSTEM ARCHITECTURE	
<u>10634597</u>	Not Issued	030	08/04/2003	ASYNCHRONOUS SYSTEM- ON-A-CHIP INTERCONNECT	LINES, ANDREW
<u>10452782</u>	Not Issued	020	05/30/2003	MESSAGE TRANSFER SYSTEM	LINES, ANDREW
<u>10354272</u>	<u>6785875</u>	150	01/28/2003	METHODS AND APPARATUS FOR FACILITATING PHYSICAL SYNTHESIS OF AN INTEGRATED CIRCUIT DESIGN	LINES, ANDREW
<u>10335343</u>	Not Issued	161	12/31/2002	PIPELINED COMPLETION FOR ASYNCHRONOUS COMMUNICATION	LINES, ANDREW M.
<u>10294044</u>	Not Issued	020	07/18/2001	RESHUFFLED COMMUNICATIONS PROCESSES IN PIPELINED ASYNCHRONOUS CIRCUITS	LINES, ANDREW M.
<u>10281424</u>	Not Issued	030	10/25/2002	TECHNIQUES FOR FACILITATING CONVERSION BETWEEN ASYNCHRONOUS AND SYNCHRONOUS DOMAINS	LINES, ANDREW
<u>10237406</u>	Not Issued	020	09/06/2002	ASYNCHRONOUS CROSSBAR WITH DETERMINISTIC OR ARBITRATED CONTROL	LINES, ANDREW
<u>10237358</u>	Not Issued	020	09/06/2002	ASYNCHRONOUS CROSSBAR WITH DETERMINISTIC OR ARBITRATED CONTROL	LINES, ANDREW
<u>10212574</u>	Not Issued	030	08/01/2002	TECHNIQUES FOR FACILITATING CONVERSION BETWEEN ASYNCHRONOUS AND SYNCHRONOUS DOMAINS	LINES, ANDREW
<u>10137035</u>	<u>6658550</u>	150	04/30/2002	PIPELINED ASYNCHRONOUS PROCESSING	LINES, ANDREW M.
<u>10136025</u>	Not Issued	020	04/30/2002	ASYNCHRONOUS CROSSBAR WITH DETERMINISTIC OR ARBITRATED CONTROL	LINES, ANDREW
<u>09501638</u>	Not Issued	160	02/10/2000	RESHUFFLED COMMUNICATIONS PROCESSES IN PIPELINED ASYNCHRONOUS CIRCUITS	LINES, ANDREW M.
<u>09496128</u>	<u>6502180</u>	150	02/01/2000	ASYNCHRONOUS CIRCUITS WITH PIPELINED COMPLETION PROCESS	LINES, ANDREW M.

<u>09360468</u>	Not Issued	160	07/22/1999	RESHUFFLED COMMUNICATIONS PROCESSES IN PIPELINED ASYNCHRONOUS CIRCUITS	LINES , ANDREW M.
<u>09151334</u>	<u>6038656</u>	150	09/11/1998	PIPELINED COMPLETION FOR ASYNCHRONOUS COMMUNICATION	LINES , ANDREW M.
<u>09118140</u>	<u>6381692</u>	150	07/16/1998	PIPELINED ASYNCHRONOUS PROCESSING	LINES , ANDREW
<u>08010824</u>	<u>5557714</u>	150	01/29/1993	METHOD AND SYSTEM FOR ROTATING A THREE- DIMENSIONAL MODEL ABOUT TWO ORTHOGONAL AXES	LINES , ANDREW

Inventor Search Completed: No Records to Display.

Search Another: Inventor **Last Name** **First Name**

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)